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U.S. UTILITY Patent Application

PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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Richard Eden Bruce Smetana

Power semiconductor switching devices, power converters, integrated circuit assemblies, integrated circuitry, power current switching methods, methods of forming a power semiconductor switching device, power conversion methods, power semiconductor switching device PTO-2040^f 1299

	ISSUING CLASSIFICATION					
ORIGINA	ORIGINAL CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL	CLASSIFICATION					
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TERMINAL	DRAWINGS			CLAIMS ALLOWED	
└── DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent			NOTICE OF ALI	LOWANCE MAILED	
subsequent to (date)					
has been disclaimed.	(Assistant Examiner) (Date)		4		
The term of this patent shall	~				•
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE	
				Amount Due	Date Paid
	(Primary	Examiner)	(Date)		